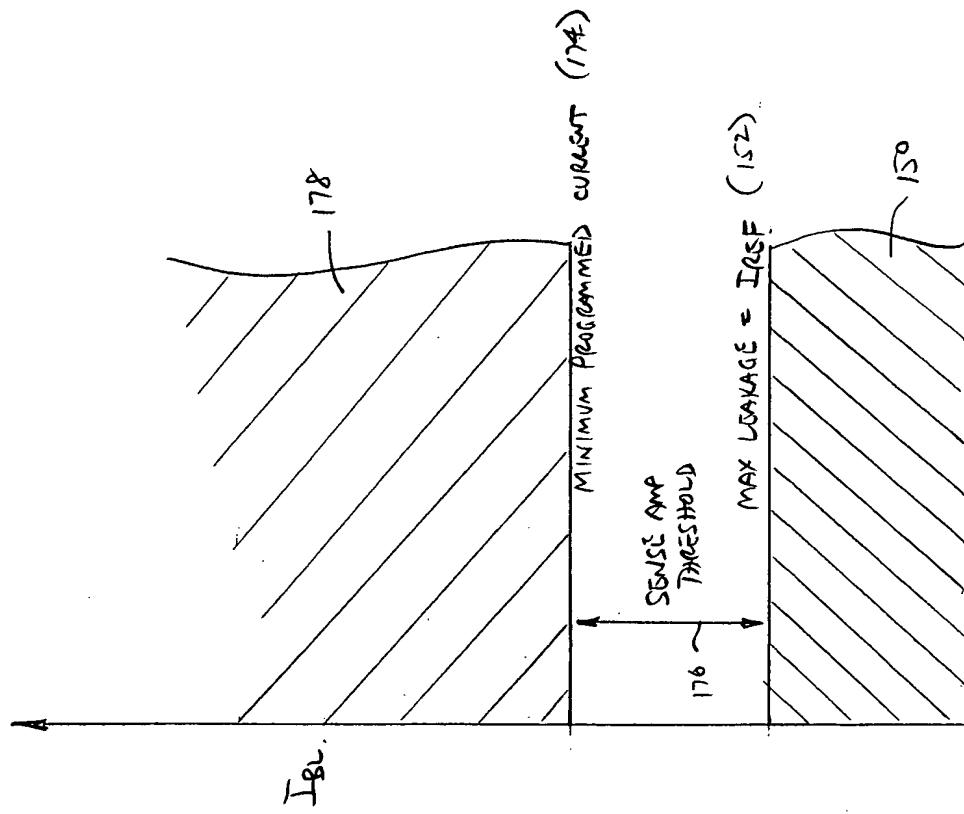
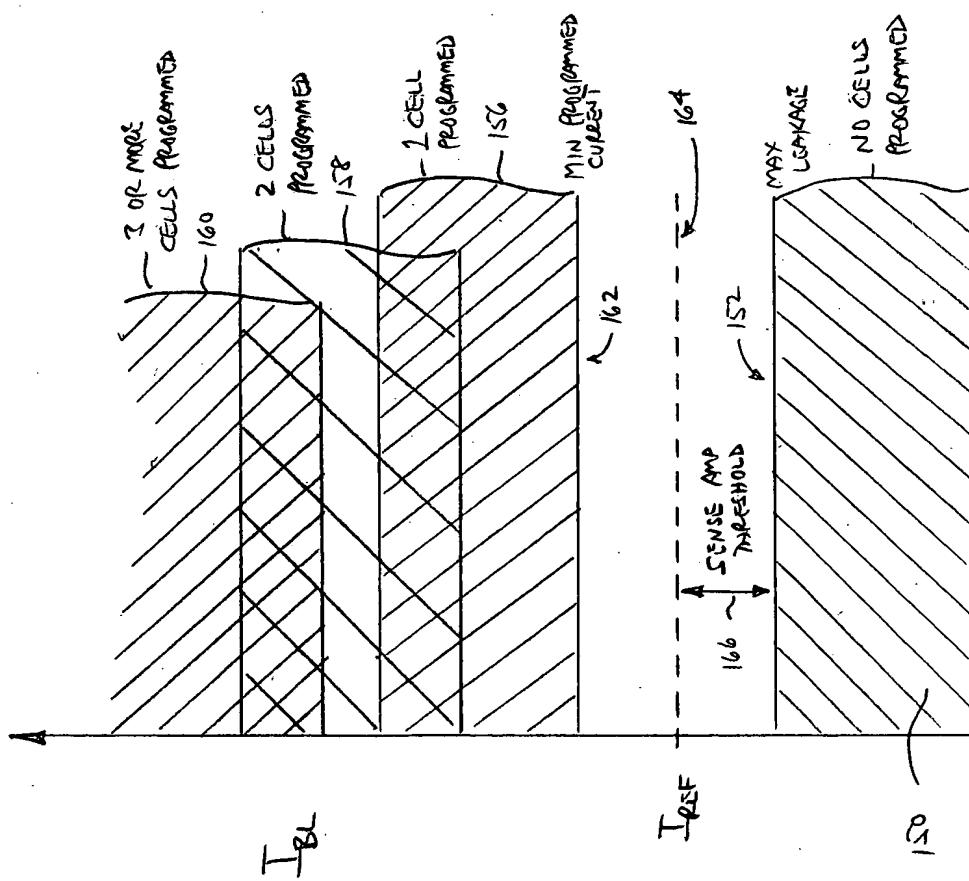


Fig. 1



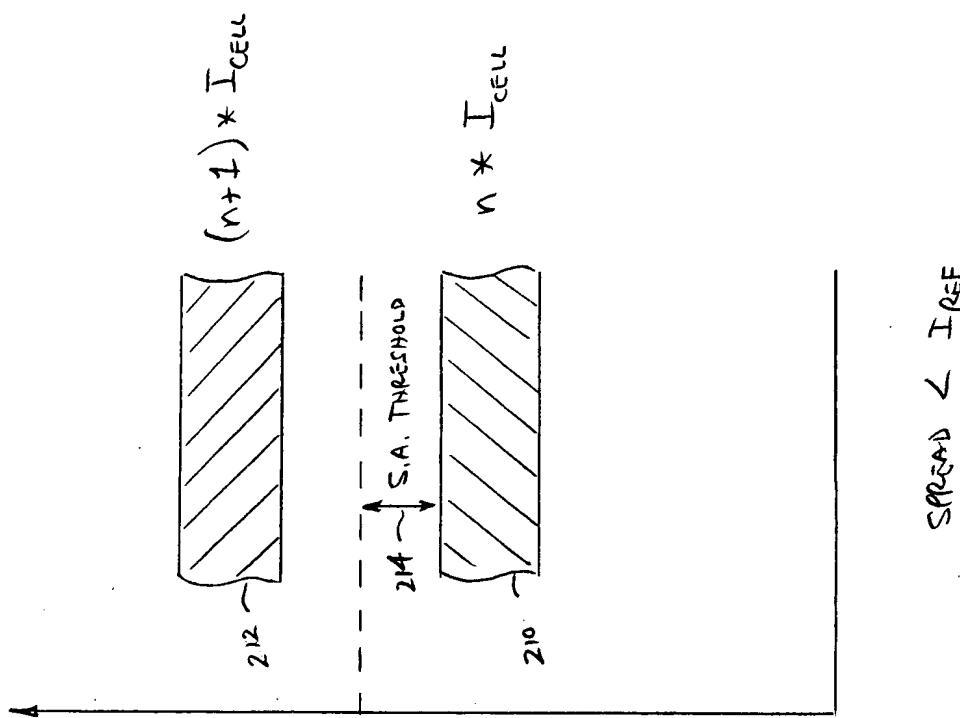
TWIN CELL

Fig. 3



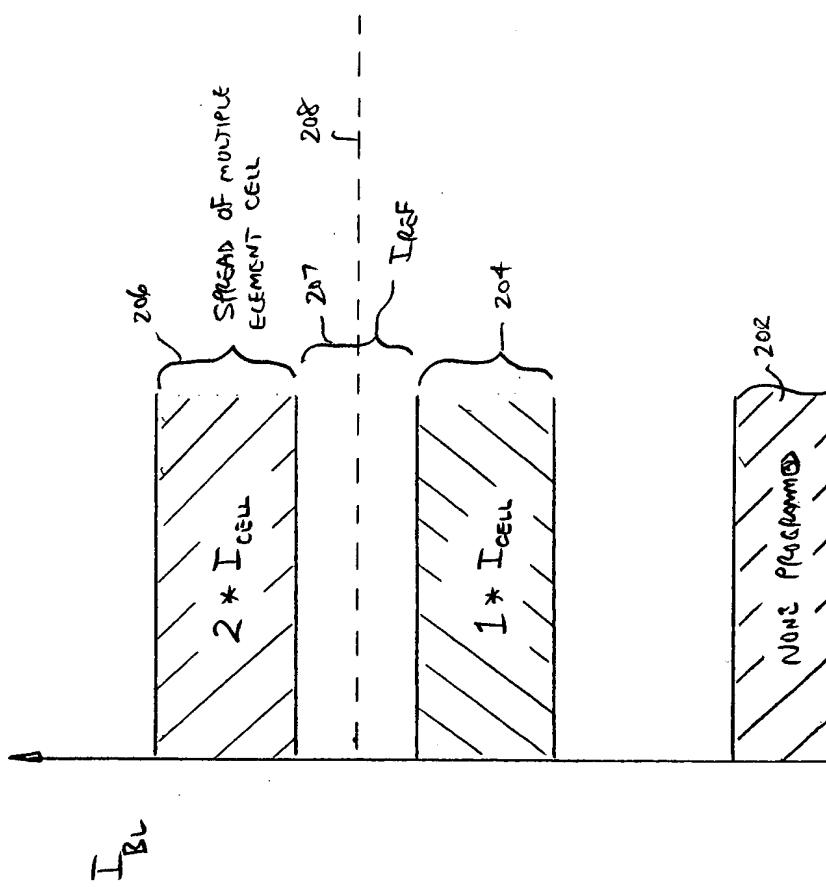
BINARY DECISION  
WITH REFERENCE

Fig. 2



$S P R E A D < I_{R E F}$

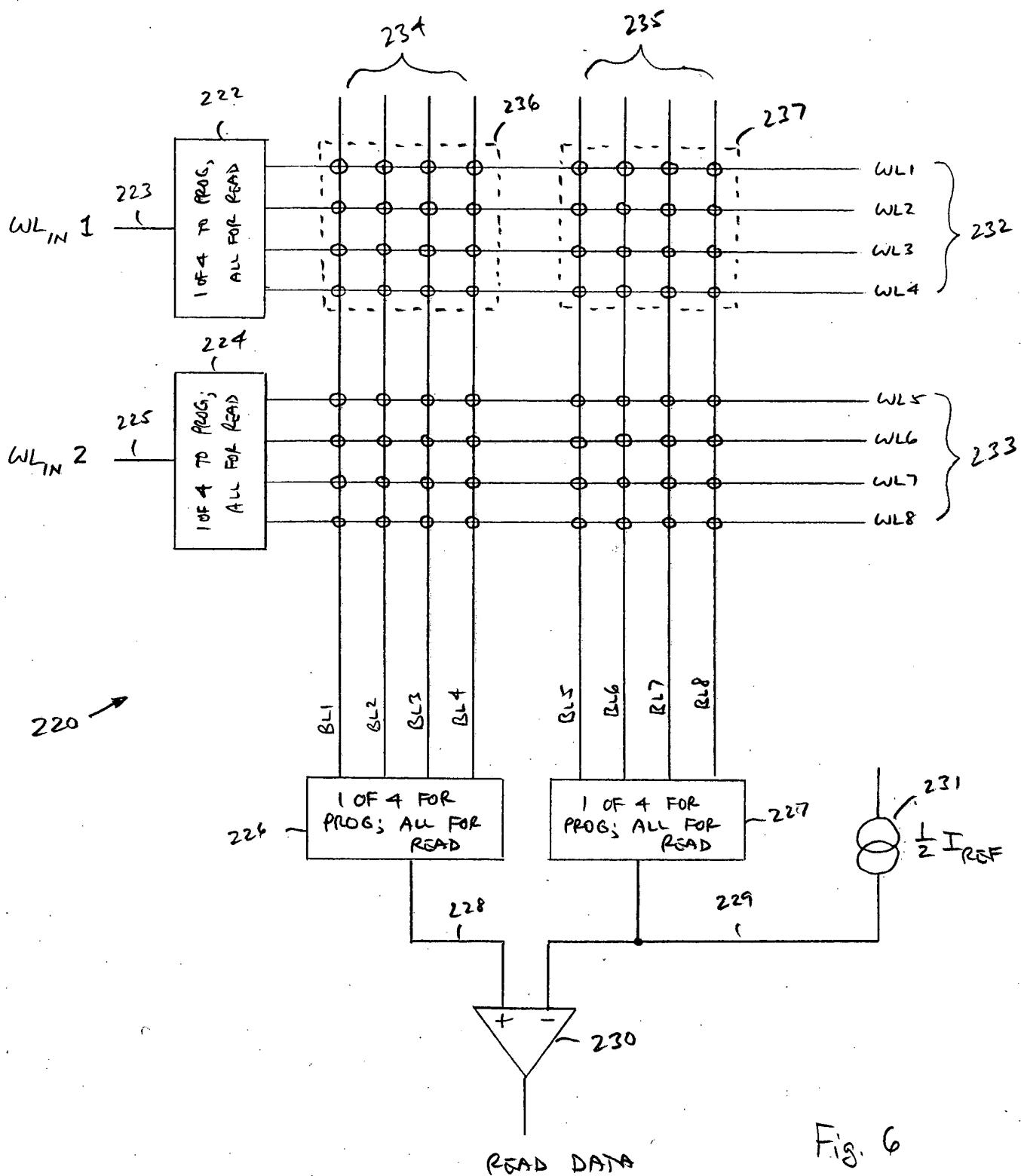
Fig. 5



$S P R E A D > I_{R E F}$

Fig. 4

Multiple Twin Cell Non-Volatile Memory Array and Logic  
Block Structure and Method Therefor  
Roy E. Scheuerlein, et al.  
Attorney Docket No.: 023-0024



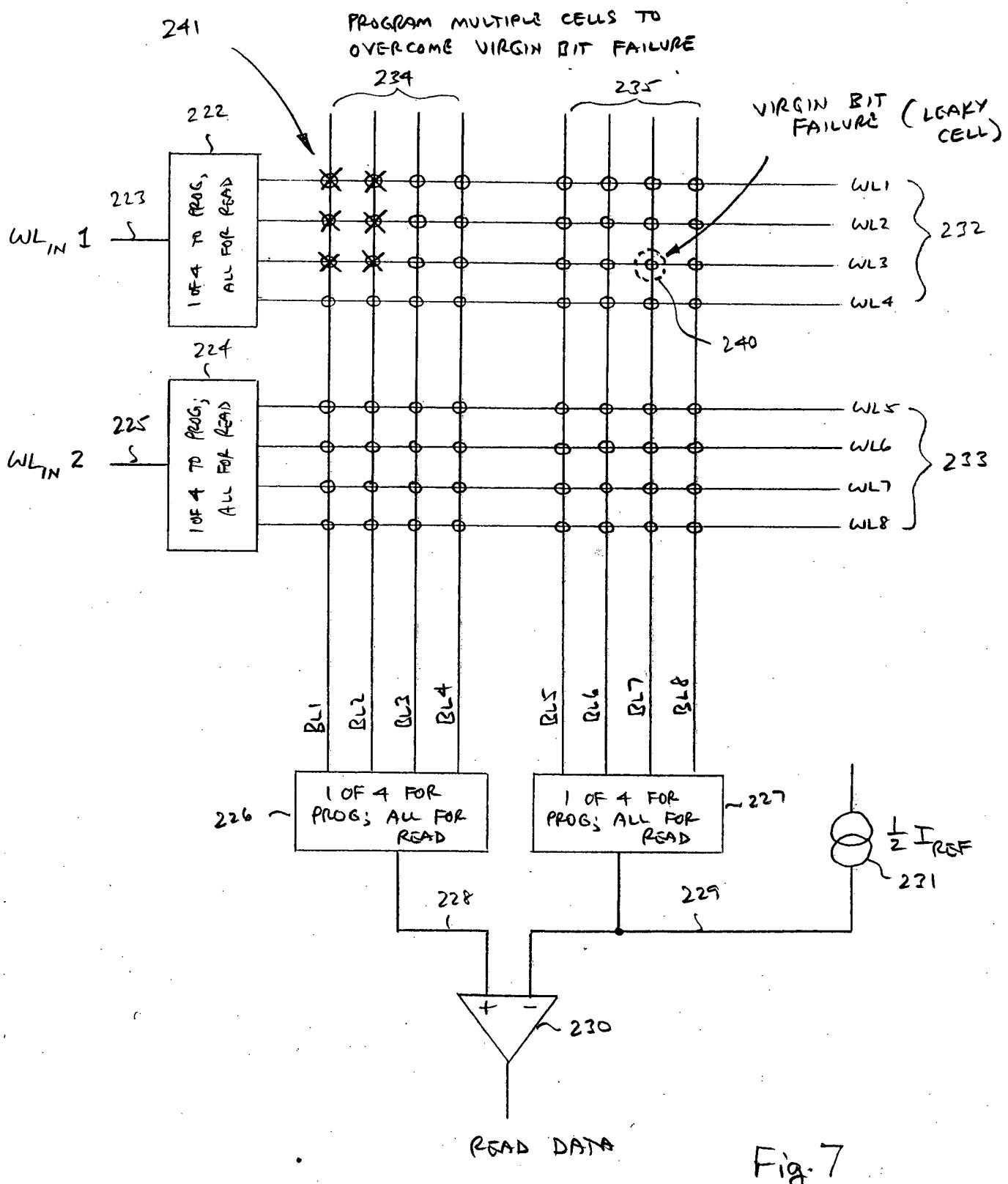
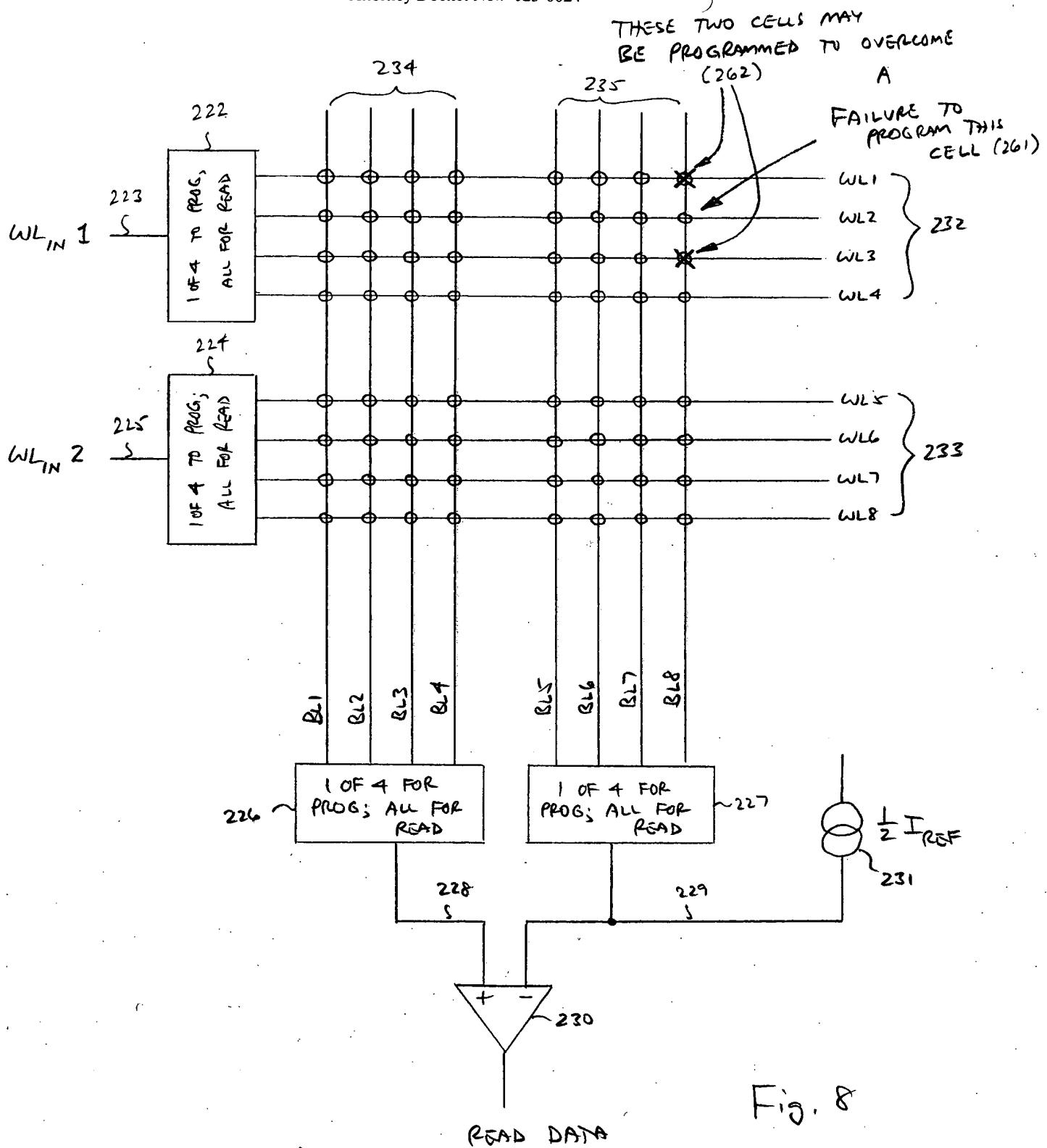


Fig. 7

Multiple Twin Cell Non-Volatile Memory Array and Logic  
Block Structure and Method Therefor  
Roy E. Scheuerlein, et al.  
Attorney Docket No.: 023-0024



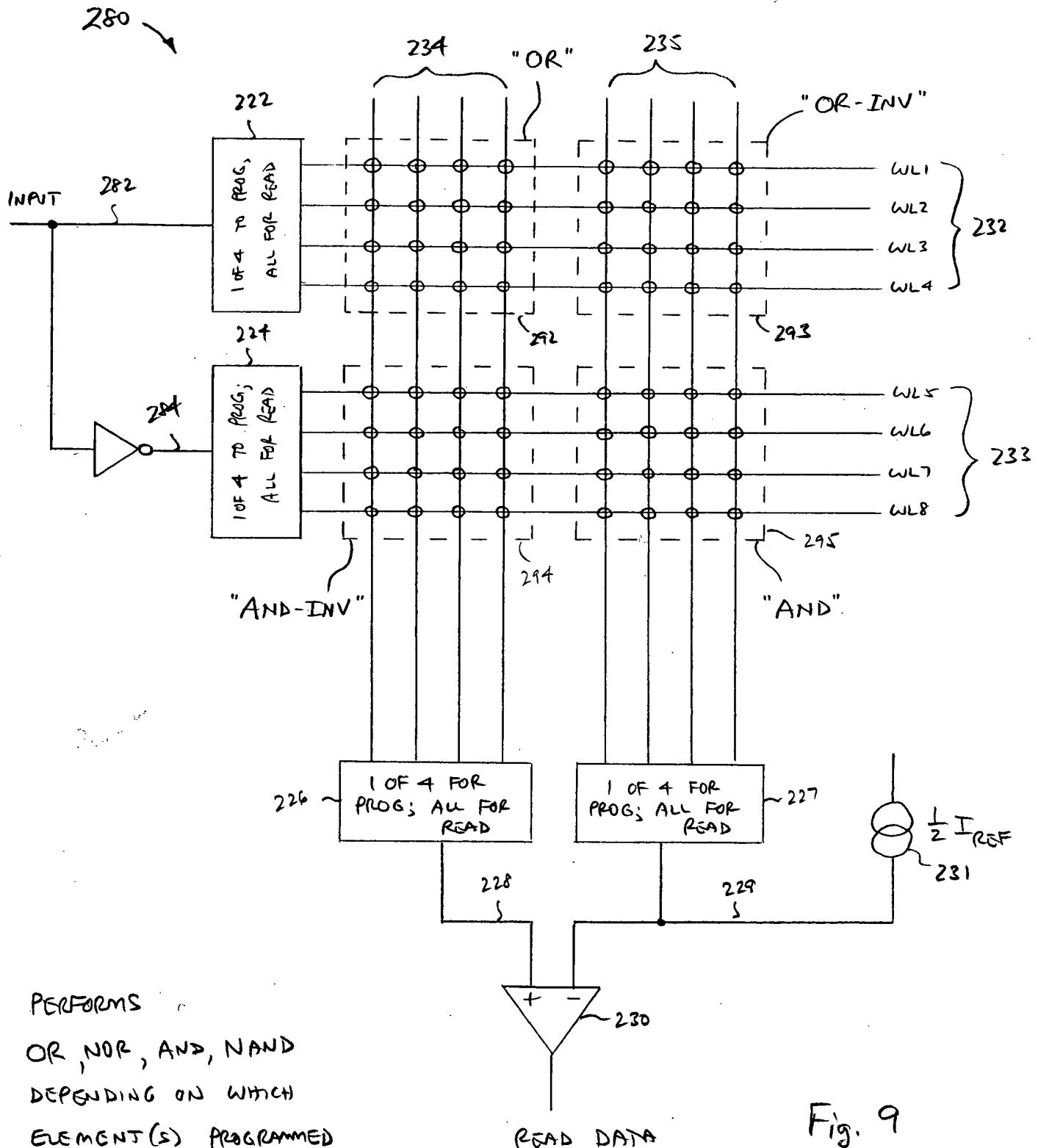


Fig. 9

**Multiple Twin Cell Non-Volatile Memory Array and Logic Block Structure and Method Therefor**  
**Roy E. Scheuerlein, et al.**  
**Attorney Docket No.: 023-0024**

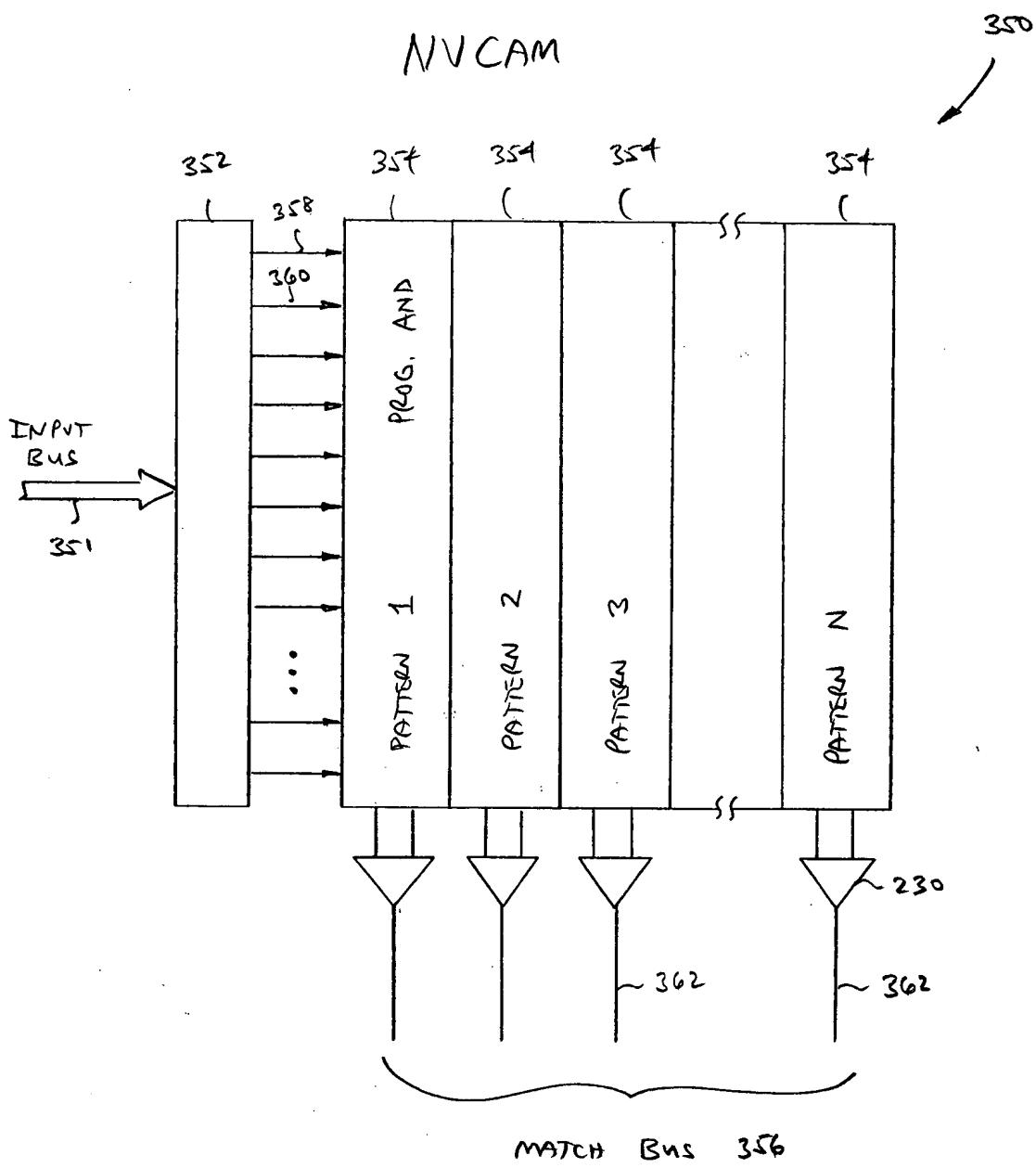


Fig. 10

